

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: M. HIRAKI, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application  
Serial No. 10/379,545, filed March 6, 2003

Filed: On even date herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,  
MEMORY MODULE, STORAGE DEVICE AND THE  
METHOD FOR REPAIRING SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE

Art Group of Parent: 2824

Examiner of Parent: H. N. Nguyen

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 15, 2003

Sir:

Prior to examination, please amend the above-identified application as listed  
below and as set forth on the following pages:

**Amendments to the Specification and the**

**Remarks follow the amendments.**